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Substitute for form 1449B/PTO		<b>Complete if Known</b>	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)		Application Number	10/721,879
		Filing Date	November 26, 2003
		First Named Inventor	DEVOR, Harold Theodore
		Group Art Unit	2183
		Examiner Name	Not yet known
Sheet	1 of 1	Attorney Docket Number	P-6216-US

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cita No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (where appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
RF	A	Hookway, R., and Herdeg, Mark A., "Digital FXI32: Combining Emulation and Binary Translation". Digital Technical Journal, Vol. 9, No. 1, 1997. Pages 3-12.	<input type="checkbox"/>
RF	B	Baraz, et al., "IA-32 Execution Layer: a two-phase dynamic translator designed to support IA-32 applications on Itanium®-based systems". Proceedings of the 36 <sup>th</sup> International Symposium on Microarchitecture (MICRO-36 2003).	<input type="checkbox"/>
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Examiner Signature	<i>[Signature]</i>	Date Considered	2/13/06
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## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet

1

of

2

Attorney Docket Number

**P-6216-US**

## U.S. PATENT DOCUMENTS

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**FOREIGN PATENT DOCUMENTS**

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RF	D	Copy of International Search Report for PCT/US2004/035246, mailed on  February 18, 2005	<input type="checkbox"/>
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Examiner Signature	<i>Robert J. Turner</i>	Date Considered	2/13/06
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